

P.002/005 F-509 T-334

## Attorney Docket No.: NECW 18.159

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor:

Atsushi NISHIZAWA

Serial No.:

09/751,979

Filed:

December 29, 2000

Title:

MANUFACTURING METHOD OF SEMICONDUCTOR INTEGRATED CIRCUIT INCLUDING SIMULTANEOUS FORMATION OF VIA HOLE REACHING METAL WIRING

AND CONCAVE GROOVE IN INTERLAYER FILM AND

SEMICONDUCTOR INTEGRATED CIRCUIT MANUFACTURED

WITH THE MANUFACTURING METHOD

Examiner:

George A. Goudreau

Group Art Unic

1763

**FAX RECEIVED** 

OFFICIAL

NOV 1 9 2002

Assistant Commission for Patents Washington, D.C. 20231

**GROUP 1700** 

CHANGE IN CORRESPONDENCE ADDRESS

SIR:

It is respectfully requested that any future correspondence regarding the above-referenced application be directed to Applicant's attorneys (previously appointed) at the following new correspondence address:

> Kanen Muchin Zavis Rosenman 575 Madison Avenue New York, New York 10022

Phone: (212) 940-8800

Fax: (212) 940-8986 or (212) 940-8987

Any inquiries regarding this change in address may be directed to the Applicant's undersigned attorney at the above-referenced telephone numbers.

Reg. No. 30,659

Our CUSTOMER NUMBER 026304 remains the same.

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